| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-----------|------|--|----------------------------|---------------------|---------|------------------|
| S1 | 2 | ("3872245").PN. | US-RGRUB; USPAT; EPO | OR | OFF | 2005/01/04 13:28 |
| S3 | 5 | (("5,587,738") or ("5,698,844") or ("5,288,988") or ("5,424,529") or ("Re34,309")).PN. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/01/04 13:45 |
| S5 | 43 | switch with clock adj signal with external\$2 with (driv\$3 controll\$3) with chip | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/04 13:52 |
| S7 | 78 | semiconductor adj chip with clock adj signal with external\$3 | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/04 13:58 |
| S23 . | 237 | imag\$3 adj2 (device sensor) and semiconductor and (external\$3 and internal\$3) with clock | US-RGRUB; USPAT; EPO | OR | ON | 2005/01/04 15:28 |
| S25 | 167 | imag\$3 adj2 (device sensor) and semiconductor and external\$3 adj \$5controller | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/04 16:48 |
| S27 | 74 | imag\$3 adj2 (device sensor) and chip and drive adj pulse and microcomputer | US-RGPUB; USPAT; EPO | OR | ON | 2005/01/04 15:57 |
| S30 | 15 | imag\$3 adj2 (device sensor) and semiconductor adj chip and external\$3 with \$5controller same clock | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/04 16:55 |
| S37 | 228 | semiconductor adj chip with imag\$3 adj (device sensor) | US-PGPUB; USPAT; EPO | OR. | ON | 2005/01/05 09:36 |
| S46 | 34 | semiconductor adj chip same imag\$3 adj (device sensor) and clock adj signal | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/05 09:39 |
| S49 | 82 | (327/296).CCLS. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/01/06 14:21 |
| S59 | 3283 | (348/294,312,211.4,211.4,207.1, 552,65,73,220.1,207.99,250). CCLS. | US-PGPUB; USPAT; EPO | OR | OFF | 2005/01/06 14:22 |
| S62 | 95 | S59 and external\$2 with clock | US-RGPUB; USPAT; EPO | OR | ON | 2005/01/05 13:34 |
| S63 | 15 | S59 and external\$2 with second with clock | US-PGPUB; USPAT; EPO | OR | ON | 2005/01/05 14:33 |